

## Process Change Notice #1008251

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <a href="http://www.silabs.com/profile">http://www.silabs.com/profile</a>

PCN Date: 25Aug10			Effective Date: 25Nov10				
<b>Title:</b> C8051F99x Revision B							
Originator: Steve Gerber	Phone: 512-532-58		Dept: EMS				
Customer Contact: Kathy Haggar	<b>Phone:</b> 512	2-532-5261	Dept: Sales				
PCN Type:							
☐ Assembly ☐ Disco	ontinuance	☐ Package		Test			
☐ Datasheet ☐ Fabr	ication		Revision				
☐ Packing ☐ Labe	ling	Locatio	n 🗆	Other			
Last Order Date: 25Nov10							
	PCI	N Details					
Description of Change:							
Silicon Laboratories (Silicon Labs) is pleased to announce the elimination of all six Rev A errata with the release of the C8051F99x Rev B family of low-power capacitive sensing MCUs. The eliminated errata include repair of the capacitive sense pin monitor, CSOMD1 bit mapping, P1.4 ADC input, the SmaRTClock startup time, reduction in non-sleep mode current, and indeterminant CRCOCN writes.							
Reason for Change:							
Eliminate the six previously identified C8051F99x revision A erratta. Three of these errata had no available workaround (P1.4 ADC input, SmaRTClock startup time, and reduction in non-sleep mode current) in Rev A. All six errata have been eliminated in Rev B.							
Impact on Form, Fit, Function,	Quality, Reliab	oility:					
C8051F99x Rev B eliminates all ki functionally compatibile to Rev A							
Product Identification:							
This PCN applies to devices ordered using the following ordering part numbers							
	Affected Orde	ering Part Nur	nbers	]			
C8051F990	0-GM C805	1F996-GM	C8051F996-GU				
Note: Affected Part Numbers also include all custom parts made from the above listed base parts and tape and reel equivalents ("R" suffix).							
Last Date of Unchanged Product	:: 25Nov10						
Subsequent to the Effective Date C8051F99x orders. The ordering improvement.							
Qualification Samples:							
Samples are available now. Please contact your local Silicon Laboratories sales representative to order samples. A list of authorized sales representatives may be found at <a href="https://www.silabs.com">www.silabs.com</a>							



# **Process Change Notice #1008251**

Customer Early Acceptance Sign Off:
Customers may approve early PCN acceptance by completing the information below:
Early Acceptance Date:
Name:
Company:
Email your early Acceptance approval to: <u>katherine.haggar@silabs.com</u>
Qualification Data: See Appendix A for the Qualification Report

## Process Change Notice #1008251

### Appendix A: Qualification Report



Preliminary C8051F99x Qualification Report
The information contained in this document is PROPRIETARY to Silicon Laboratories, Inc. and shall not be reproduced or used in part or whole without Silicon Laboratories' written consent. The document is uncontrolled if printed or electronically saved.

Part Rev C8051F99x, TSMC 0.18um Fabrication										
			Lot ID or	Fail/Pass						
Test Name	Test Condition	Qualification	Start	or End	Notes	Summary	Status			
Test Group B - A	Accelerated Lifetime Simulati	on Tests								
HTOL	JA108		Q27065	0/88						
	125°C, Dynamic	3 lots, N=>77	Q28301	0/80		2 lots	Ongoing			
	Vcc=3.3V, 1000 hours		Q28955	f/p		0/168				
LTOL	JA108									
	-10°C, Dynamic	1 lot, N=>32	Q25548	0/39		1 lots	Pass			
	Vcc=3.3V, 1000 hours					0/39				
ELFR	JA108		Q27592	0/520						
	125°C, Dynamic	3 lots, N=>500	Q28218	0/516		2 lots	Ongoing			
	Vcc=3.3V, 48 hours		Q28868	f/p		0/1036				
Test Group E - E	Electrical Verification									
ESD-HBM	JA114									
		1 lot, N=>3	Q28863				2kV			
ESD-MM	JA115									
		1 lot, N=>3	Q28865				225V			
ESD-CDM	JC101									
		1 lot, N=>3	Q28864				2kV			
Latch Up	JESD78									
	±200mA	1 lot, N=>6	Q28867	85C			Pass			
	Overvoltage = 3.6V		Q28866	25C						